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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	3	of	3
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Complete If Known

Application Number	08/801,628
Filing Date	03/08/2001
First Named Inventor	Keeth et al.
Group Art Unit	2818
Examiner Name	Not yet assigned
Attorney Docket Number	DB000575-010

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

[illegible]

**Examiner
Signature**

4. TRAN

Date Considered

9	21	03
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¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 3

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Application Number	08/801,628
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First Named Inventor	Keeth et al.
Group Art Unit	2818
Examiner Name	Not yet assigned
Attorney Docket Number	DB000575-010

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number	Kind Code ² (if known)			class	sub
MS		6,043,118		Suwanai et al.	03/28/2000	438	253
		5,960,455		Bauman	09/28/1999	711	120
		5,838,627		Tomishima et al.	11/17/1998	365	230.03
		5,677,645		Merritt	10/14/1997	327	536
		5,651,011		Keeth	07/22/1997	371	22.1
MS		5,614,859		Ong	03/25/1997	327	333
		5,574,697		Manning	11/12/1996	365	226
		5,557,579		Raad et al.	09/17/1996	365	226
		5,552,739		Keeth et al.	09/03/1996	327	538
		5,526,364		Roohparvar	06/11/1996	371	22.1
MS		5,519,360		Keeth	05/21/1996	331	57
		5,481,179		Keeth	01/02/1996	323	315
		5,379,263		Ogawa et al.	01/03/1995	365	230.04
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		5,266,821		Chern et al.	11/30/1993	257	312.
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		5,212,440		Waller	05/18/1993	323	313
		5,159,273		Wright et al.	10/27/1992	324	537
		5,155,704		Walther et al.	10/13/1992	365	201
		4,970,725		McEnroe et al.	11/13/1990	371	15.1

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner
Signature

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Date _____

Considered

91/2002

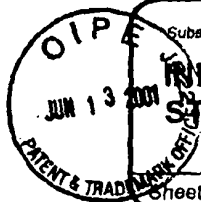
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 3

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Application Number	08/801,628
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Group Art Unit	2818
Examiner Name	Not yet assigned
Attorney Docket Number	DB000575-010

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume/issue number(s), publisher, city and/or country where published.	T ²
ME		SUGIBAYASHI ET AL., "A 30-ns 256-Mb DRAM with a Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993)	
		TAGUCHI ET AL., "A 40-ns 64-Mb DRAM with 64-b Parallel Data Bus Architecture", IEEE Journal of Solid-State Circuits, Vol. 26 (No. 11), (November 1991)	
		KITSUKAWA ET AL., "256-Mb DRAM Circuit Technologies for File Applications", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993)	
ME		JEDEC SOLID STATE PRODUCTS ENGINEERING COUNCIL, "Committee Letter Ballot", JC-42.3-95-73, Item #633.13, Arlington, VA (April 1995)	
		YOO ET AL., "SP 23.6: A 32-Bank 1Gb DRAM with 1 GB/s Bandwidth", ISSCC96/Session 23/DRAM/PAPER SP 23.6	
		NITTA ET AL., "SP 23.5: A 1.6GB/s Data-Rate 1Gb Synchronous DRAM with Hierarchical Square-Shaped Memory Block and Distributed Bank Architecture", ISSCC96/Session 23/DRAM PAPER SP 23.5	
		U.S. Patent Application S.N. 08/521,563, entitled IMPROVED VOLTAGE REGULATOR CIRCUIT, filed 8/30/95	
ME		U.S. Patent Application S.N. 08/668,347, entitled DIFFERENTIAL VOLTAGE REGULATOR, filed 6/26/96	
		U.S. Patent Application S.N. 08/460,234, entitled SINGLE DEPOSITION LAYER METAL DYNAMIC RANDOM ACCESS MEMORY, filed 8/17/95	
		U.S. Patent Application S.N. 08/420,943, entitled DYNAMIC RANDOM-ACCESS MEMORY, filed 6/4/95	
ME		U.S. Patent Application S.N. 08/194,184, entitled INTEGRATED CIRCUIT POWER SUPPLY HAVING PIECEWISE LINEARITY, filed 2/8/94	

Examiner Signature	U. TRAN	Date Considered	9/21/03
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